



01/17/02

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Vishnu K. Agarwal et al.

Title: TRANSISTOR STRUCTURE HAVING REDUCED TRANSISTOR LEAKAGE ATTRIBUTES

Attorney Docket No.: 303.780US1

JC979 U.S. PTO
10/053300
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PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Commissioner for Patents

Washington, D.C. 20231

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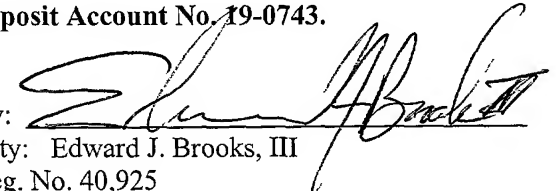
- ☒ • Return postcard.
- ☒ Utility Patent Application under 37 CFR § 1.53(b) comprising:
- ☒ Specification (29 pgs, including claims numbered 1 through 77 and a 1 page Abstract).
- ☒ Formal Drawing(s) (8 sheets).
- ☒ **Signed Declaration** (4 pgs).
- ☒ Signed Power of Attorney (1 pg.)
- ☒ Check in the amount of \$3,278.00 to pay the filing fee.
- ☒ Assignment of the invention to Micron Technology, Inc. (4 pgs) and Recordation Form Cover Sheet.
- ☒ Check in the amount of \$40.00 to pay the Assignment recording fee.

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	77 - 20 =	57	x 18 =	\$1,026.00
INDEPENDENT CLAIMS	21 - 3 =	18	x 84 =	\$1,512.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$740.00
TOTAL				\$3,278.00

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